

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S17	1282	specify\$3 same interconnect\$3 same (component cell block)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:23
S18	511	specify\$3 same interconnect\$3 same (component cell block) and optimiz\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:23
S20	31	specify\$3 same interconnect\$3 same (component cell block) same optimiz\$5 and @ad<"20040130"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:34
S21	33	specify\$3 same interconnect\$3 same (component cell block device) same optimiz\$5 and (bandwidth latency scalability) and @ad<"20040130"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:46
S22	4	specify\$3 same interconnect\$3 same (component cell block device) same optimiz\$5 and (bandwidth latency scalability) and (self adj2 programmable) and @ad<"20040130"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:47
S23	24	specify\$3 same interconnect\$3 same (component cell block device) same optimiz\$5 and (bandwidth latency scalability) and (programmable) and @ad<"20040130"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:49
S24	4	specify\$3 same interconnect\$3 same (component cell block device) same optimiz\$5 and (bandwidth latency scalability) and (self near4 programmable) and @ad<"20040130"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:49
S25	4	specify\$3 same interconnect\$3 same (component cell block device) same optimiz\$5 and (bandwidth latency scalability) and (self-programmable) and @ad<"20040130"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:50

S26	4	specify\$3 same interconnect\$3 same (component cell block device) same optimiz\$5 and (bandwidth latency scalability) and (self-programmable self-configurat\$3) and @ad<"20040130"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:51
S27	4	specify\$3 same interconnect\$3 same (component cell block device) same optimiz\$5 and (bandwidth latency scalability) and (self?programmable) and @ad<"20040130"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:54
S28	4	specify\$3 same interconnect\$3 same (component cell block device) same optimiz\$5 and (bandwidth latency scalability) and (heuristic) and @ad<"20040130"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:54
S29	4	specify\$3 same interconnect\$3 same (component cell block device) same optimiz\$5 and (bandwidth latency scalability) and (heuristic intelligent) and @ad<"20040130"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:55
S30	3	specify\$3 same interconnect\$3 same (component cell block device) same optimiz\$5 and (bandwidth latency scalability) and (intelligent) and @ad<"20040130"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:56
S31	62	specify\$3 same interconnect\$3 same (component cell block device) and optimiz\$5 and (bandwidth latency scalability) and (intelligent) and @ad<"20040130"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:56
S32	5	specify\$3 same interconnect\$3 same (component cell block device) and optimiz\$5 and (bandwidth latency scalability) and (intelligent) and @ad<"20040130" and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:57
S33	4	specify\$3 same interconnect\$3 same (component cell device) and optimiz\$5 and (bandwidth latency scalability) and (intelligent) and @ad<"20040130" and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:57

S34	18	design\$3 same interconnect\$3 same (component cell device) and optimiz\$5 and (bandwidth latency scalability) and (intelligent) and @ad<"20040130" and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 16:58
S35	7	(design\$3 same interconnect\$3 same (component cell device) same optimiz\$5) and (bandwidth latency scalability) and (intelligent) and @ad<"20040130" and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 17:05
S36	5	(design\$3 same interconnect\$3 same (component cell device) same optimiz\$5) and (bandwidth latency scalability) and (intelligent) and specify\$3 and @ad<"20040130" and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 17:08
S37	15	(design\$3 same interconnect\$3 same (component cell device) same optimiz\$5) and (bandwidth latency scalability) and (heuristic agent intelligent) and specify\$3 and @ad<"20040130" and "716"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 17:08
S38	2	"6751783".pn. and self?programmable	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/04 17:20
S63	1	"6467010".pn. and synthesis	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 16:07
S64	0	"6467010".pn. and synthesis and optimiz\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 16:07
S65	0	"6467010".pn. and synthesis and optimiz\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 16:40

S67	0	"6539531".pn. and optimiz\$3 and synthesiz\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 16:41
S68	1	"6539531".pn. and optimiz\$3 and synthes\$6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 16:41
S69	41	specify\$3 same interconnect\$3 same (component cell block) same optimiz\$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 16:52
S70	366	(interconnect\$3 adj3 information) and (bandwidth latency) and optimiz\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 17:11
S71	19	(interconnect\$3 adj3 information) and (bandwidth latency) and optimiz\$3 adj4 design	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 17:15
S72	66	(interconnect\$3 adj3 information) and (optimiz\$3 adj4 design)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 17:14
S73	602	(interconnect\$3) and (bandwidth latency) and optimiz\$3 adj4 design	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 17:16
S74	20	(interconnect\$3) near5 (optimiz\$3 adj4 design) and (bandwidth latency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 17:18

S75	32	(interconnect\$3) near10 (optimiz\$3 adj4 design) and (bandwidth latency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 17:20
S76	27	(interconnect\$3) near10 (optimiz\$3 adj4 design) and (bandwidth latency) and specify\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/11/06 17:21